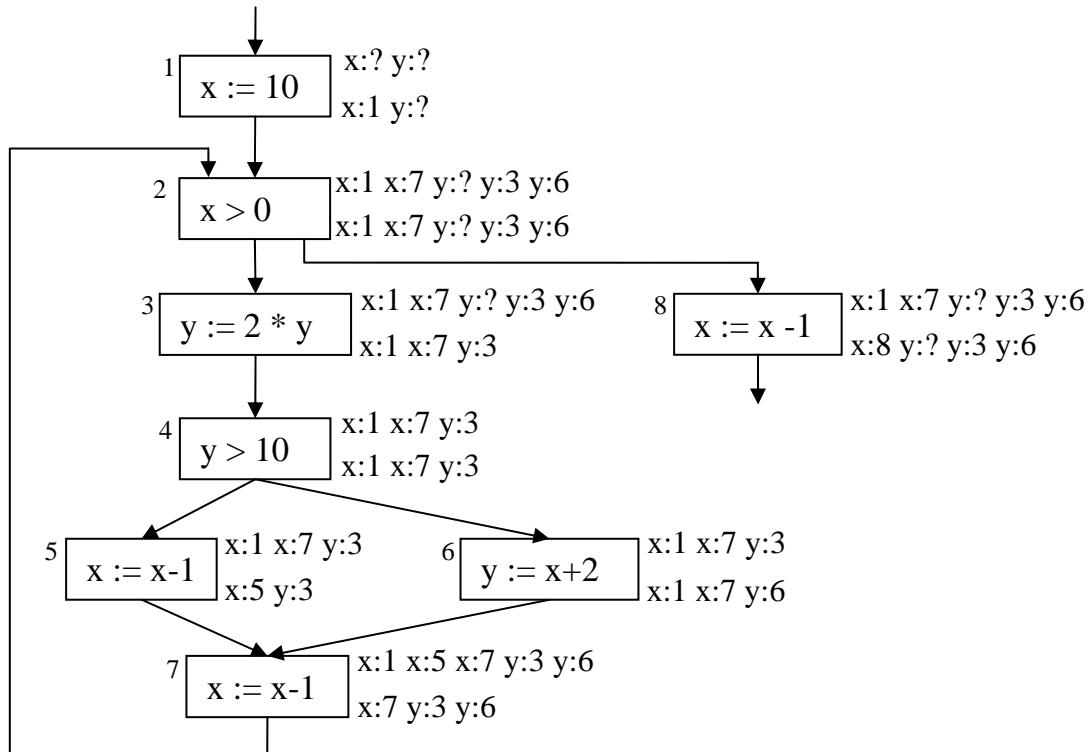


Software Verification

Exercise Solution: Data Flow Analysis

1 Reaching definitions analysis

(a) and (b)



(c)

Block	x	y
1	\emptyset	\emptyset
2	1,7	\emptyset
3	\emptyset	?3,6
4	\emptyset	3
5	1,7	\emptyset
6	1,7	\emptyset
7	1,5,7	\emptyset
8	1,7	\emptyset

2 Live variables analysis

(a)

```
[x := y]1
[x := x - 1]2
[x := 4]3
while [y < x]4 do
    [y := y + x]5
end
[y := 0]6
```

(b)

$$\begin{aligned} LV_{entry}(1) &= LV_{exit}(1) \setminus \{x\} \cup \{y\} \\ LV_{entry}(2) &= LV_{exit}(2) \setminus \{x\} \cup \{x\} \\ LV_{entry}(3) &= LV_{exit}(3) \setminus \{x\} \\ LV_{entry}(4) &= LV_{exit}(4) \cup \{x,y\} \\ LV_{entry}(5) &= LV_{exit}(5) \setminus \{y\} \cup \{x,y\} \\ LV_{entry}(6) &= LV_{exit}(6) \setminus \{y\} \end{aligned}$$

$$\begin{aligned} LV_{exit}(1) &= LV_{entry}(2) \\ LV_{exit}(2) &= LV_{entry}(3) \\ LV_{exit}(3) &= LV_{entry}(4) \\ LV_{exit}(4) &= LV_{entry}(5) \cup LV_{entry}(6) \\ LV_{exit}(5) &= LV_{entry}(4) \\ LV_{exit}(6) &= \emptyset \end{aligned}$$

(c)

I	LV_{entry}(I)	LV_{exit}(I)
1	y	x,y
2	x,y	y
3	y	x,y
4	x,y	x,y
5	x,y	x,y
6	\emptyset	\emptyset

(d) We eliminate block 1 of the form $x := \dots$ if x is not an element of $LV_{exit}(l)$.

```
[x := y]1
[x := 4]3
while [y < x]4 do
    [y := y + x]5
end
```

(e)

No, variable x in block 1 is still dead.

Modified equation:

$$LV_{entry}(l) = \begin{cases} \text{if } kill_{LV}(l) \subseteq LV_{exit}(l) \text{ then} \\ \quad LV_{exit}(l) \setminus kill_{LV}(l) \cup gen_{LV}(l) \\ \text{else} \\ \quad LV_{exit}(l) \end{cases}$$

Rationale behind this equation: if a block assigns a variable that is not live afterwards, then it must be eliminated, and should not influence the analysis by adding the variables it reads to the live variable set.

Modified analysis:

I	LV_{entry}(I)	LV_{exit}(I)
1	y	y
2	y	y
3	y	x,y
4	x,y	x,y
5	x,y	x,y
6	Ø	Ø

Modified code elimination:

```
[x := 4]3
while [y < x]4 do
    [y := y + x]5
end
```