Concepts of Concurrent Computation Seminar Talk

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Applying Transactional Memory to Concurrency Bugs

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> > ASPLOS' 12

What is Transactional Memory (TM)?

Paradigm for concurrent programming that eliminates the usual problems when programming with locks.

A **transaction** is a sequence of steps executed by a single processor.

Transactions are:

- Atomic: each transaction either commits (it takes effect) or aborts (its effects are discarded)
- Isolated: intermediate changes of one transaction are not visible to other transactions

There are hardware, software and hybrids TMs.

Motivation

Check utility of TM as a tool for fixing concurrency bugs.

Find mechanisms that could be useful if added in TM.

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Bug-Fixing Methods

- ► Ingredients: underlying mechanisms provided by TM.
- Recipes: methods on how to combine the mechanisms to fix bugs.

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Ingredients

- Atomic regions: ensure that transactions execute atomically and in isolation.
- **Explicit rollback** (retry): transactions can rollback partially executed transactions.

Can be used to wait until some condition is true, similar to condition variables.

- Preemptible resources: can safely be acquired inside a transaction and automatically released if the transaction aborts.
- Atomic/lock serialization: synchronizes accesses protected under a transaction with accesses protected under a lock.

Replacement of Deadlock-Prone Locks

Remove all locks contributing to a deadlock and insert transactions in their places.



If deadlock is about to occur, it is prevented by:

- serializing the threads involved
- automatically aborting one or more transactions

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(Asymmetric) Deadlock Preemption

Make at least one thread involved in the deadlock preemptible.



Removes non-preemption requirement for a deadlock.

Code wrapped in a transaction better if used infrequently.

Requires:

preemptible resources

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deadlock detector

Wrap All

Wrap all conflicting code regions into transactions.



Ensures that memory accesses are protected under the same synchronization mechanism.

Wrap Unprotected

Wrap the code region intended to be executed atomically in a transaction.



"Wrap All" recipe duplicates any existing effort put into using locks.

Requires:

atomic/lock serialization

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Recipes Comparison

	Atomic Regions	Explicit Rollback	Preemptible Resources	Atomic/Lock Serialization
Replacement of Deadlock- Prone Locks	YES	Not needed by a specific recipe	NO	NO
Deadlock Preemption			YES + deadlock detector	NO
Wrap All			YES(?)	NO
Wrap Unprotected			YES	YES

"Deadlock Preemption" and "Wrap Unprotected" easy to apply since existing locks are not removed.

Less code changes (only one transaction is added).

Better performance, less overhead by the TM.

Although they need more ingredients.

Apache: Deadlock Bug

(solved using deadlock preemption)



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Developers fix: Unlock the timeout lock before going through the conditional wait. Took them 3 failed attempts.

TM fix is 22% slower than the developers, but is preferable since:

- simple, no reasoning
- performance under stress test workload
- retry was done with spinning

Apache: Atomicity Violation Bug

(solved using wrap unprotected)



 ${\sf I}/{\sf O}$ is done using xCalls: library-based implementation of transactional semantics for common system calls.

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Apache: Atomicity Violation Bug

Developers fix: assign a lock to each log device, more scalable than using a single big lock.

TM fix is 4% slower and is preferable because:

- it is local (only changes inside one function)
- has comparable performance with the developers fix

Evaluation

Tried to use TM to fix 60 bugs in already found and fixed bugs in Mozilla, Apache HTTP Server and MySQL.

- Fixed 43 out of the 60 bugs.
- Out of the 43 TM fixes, 34 of them are preferable to the developers fixes.

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Why can't TM fix every concurrent bug?

- Locks contained in multiple modules.
- Code that needs to be changed is contained in third-party plugins.
- Design errors.
- Long-running operations.
- Two-way communication.

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Conclusion

- Straightforward use of TM has the expressive power to fix many concurrency bugs.
- But still work to be done concerning TM performance.
- TM research could be directed to tackle:
 - two-way communication
 - long-running operations

Questions?

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